

Patent**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Application of: MATHENY et al.)	Examiner: Not yet assigned
)	
Serial No.: Not yet assigned)	Group Art Unit: Not yet assigned
)	
Filing Date: Herewith)	INTEL Docket No.: P18484
)	
For: SYSTEM AND METHOD FOR)	
FLEXIBLE OPTION ROM BOOTING)	
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)	
)	
)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants submit herewith patents, publications or other information of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

The filing of this information disclosure statement shall not be construed as a representation that a thorough search has been made, an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. Nor shall the filing of this information disclosure statement be construed as an admission against interest in any manner.

This Information Disclosure Statement is filed in accordance with 37 CFR §§1.56, 1.97 and 1.98. The items listed on the accompanying Form PTO-1449 may be deemed to be pertinent to the above-identified application and are made of record to assist the Patent and Trademark Office in its examination of this application. The Examiner is respectfully requested to fully consider the items listed on the enclosed copy of Form PTO-1449 and to independently ascertain their teaching.

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Nathaniel Levin', written over a horizontal line.

March 29, 2004
Date

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not yet assigned
				Filing Date	Herewith
				First Named Inventor	MATHENY, David L.
				Group Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	1	of	1	INTEL Docket Number	P18484

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	Wade, Roy and Neshati, Ramin "The PC-AT Boot Process and Option ROMs", Intel® DeveloperUPDATEMagazine, Sept/Oct 1999, pages 1-11.	
	2	"BIOS Boot Specification", Version 1.01, January 11, 1996, Compaq Computer Corporation, Phoenix Technologies Ltd., Intel Corporation. Pages 1-46.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.